



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re Patent Application of:

Hisashi OHTANI et al.

Serial No. 09/837,552

Filed: April 19, 2001

For: SEMICONDUCTOR DEVICE AND
MANUFACTURING METHOD
THEREOF

)

) Group Art Unit: 2815

) Examiner: N. Drew Richards

) Confirmation No. 7555

) Date: September 10, 2003

REQUEST FOR ACKNOWLEDGEMENT OF IDS

**REQUEST FOR ACKNOWLEDGMENT OF
INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

Information Disclosure Statements with Forms PTO-1449 was filed in the above-identified patent application on July 14, 2003. Applicants have not yet received back from the Examiner a copy of the Forms PTO-1449 initialed to acknowledge the fact that the Examiner has considered the cited disclosed information.

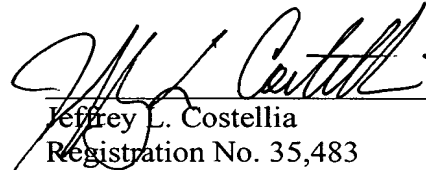
The Examiner is requested to initial and return to the undersigned a copy of the subject Forms PTO-1449.

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Should there be any questions concerning this communication, please telephone the undersigned at the number set forth below.

Respectfully submitted,



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